## EnggTree.com

Course Code/Name : EC 3251 Circuit Analysis
Regulation
: 2021
Unit I - V Slot Questions

| QNo | Unit | Questions | Status | Mar | Sign |
| :---: | :---: | :---: | :---: | :---: | :---: |
| 1 | I |  |  | 8 |  |
| 2 | I | 黄L $122 \underbrace{2}_{0}$ |  | 8 |  |
| 3 | I | 壴 |  | 8 |  |
| 4 | I | 1.14 Using nodal analysis, find $v_{o}$ in the circuit of Fig. 3.63. |  | 8 |  |
| 5 | I | 3.3 Find the currents $I_{1}$ through $I_{4}$ and the voltage $v_{0}$ in the circuit of Fig. 3.52. |  | 8 |  |
| 6 | I | Find V1,V2,V3 uisng Nodal Analysis |  | 8 |  |

## Downloaded from EnggTree.com



## Downloaded from EnggTree.com






Prepared By
Verified By
Approved By

Downloaded from EnggTree.com

